



What is a layout design of integrated circuit?

The layout design of integrated circuit is an image of a three-dimensional arrangement, expressed in any way, of the elements contained in the integrated circuit, and the connections between all or some of the elements, or of a three-dimensional arrangement designed to be used in the manufacture of the integrated circuit, with at least one element being an active element.

The essential characteristics of the the integrated circuit, such as dimensions, energy consumption, operating speed, etc. depend on the layout of the elements and connections.

Last modified 25.09.2019

Source URL: <https://www.epa.ee/en/european-patents/what-layout-design-integrated-circuit>